# B.Tech. (EE) 4th Semester F-Scheme

# Examination, May-2019

# ANALOG ELECTRONICS

#### Paper-EE-202-F

[Maximum marks: 100

Note: Question No. 1 is compulsory. Attempt one question from each section.

1.	(a)	What is importance of P-N Junction diode?	4
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- Differentiate between BJT and MOSFET.
- What is function of BJT? 2
- Define PSRR and CMMR.
- List out the advantage of -ve feedback
- Why differential pair configuration is required in transistors?

#### Section-A

- What is a P-N Junction? Explain the formation of potential barrier of a P-N junction. 10
  - Explain the following breakdown mechanisms occurring in junction: 10
    - Avalanche breakdown
    - Zener Breakdown

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- 3. Discuss the different multiplier circuits with neat and clean diagrams.
  - Find the Forward current for germanium diode at room temperature 27°C when voltage across it is 0.5 volt and compare with current when temperature rises to 80°C.

#### Section-B

- Explain the operation of MOSFET transistor in Enhancement mode. 10
  - Draw and explain the frequency respond of common source (CS) amplifier. 10
- 5. Draw and explain the smell signal model of MOSFET.
  - Explain the different biasing circuits of MOSFET amplifiers. https://www.haryanapapers.com 10

## Section-C

- Discuss the different configuration of Transistor 6. and their operation.
  - A germanium transistor with  $\alpha = 0.98$  gives a reverse saturation current  $I_{CBO} = 15\mu A$  in CB configuration. When transistor is used in CE configuration with a base of 0.22µA. Calculate the Collector current. 08

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 (a) Draw and explain the high frequency model of CE Amplifier.

 (b) Explain the voltage divider biasing circuit used in transistors.

### Section-D

- 8. (a) Draw and explain the Pin diagram of op-Amp IC.
  - (b) Discuss the different topologies of feedback
    Amplifier. 10
- 9. Write a short note on:

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- (a) Difference Amplifier
- (b) MOSFET differential pair
- (C) op-Amp as Inverting Amplifier

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