

24004

**B.Tech. 2nd Semester F-Scheme
(Common for All Branches) Examination,**

May-2019

BASICS OF ELECTRONICS

Paper-ECE-101-F

Time allowed : 3 hours] [Maximum marks : 100

Note : Question No. 1 is compulsory. Attempt any one question from each section.

- 1. (i) What is the amplifier and why their is need for cascading ? 4
- (ii) What is CMRR and Slew rate ? 4
- (iii) What are the differences between Zener and Avalanche breakdown ? 4
- (iv) What is the use of acquadag coating ? Discuss. 4
- (v) Discuss De-Morgan's theorem. 4
- 2. (a) What is PN junction ? How it is formed ? Explain VI characteristics of junction diode. 10
- (b) Explain the differences between intrinsic and extrinsic semiconductor. 10

SECTION – A

- 2. (a) What are different techniques of method study ? 10
- (b) What are various incentive payment schemes ? 10
- 3. (a) Explain the behavioural aspects in detail. 10
- (b) Explain the various factors effecting the productivity. 10

SECTION – B

- 4. Write short notes on the following : 20
 - (i) EOQ
 - (ii) ABC
- 5. (a) Explain the cost variance analysis. 10
- (b) What is fixed and variable cost ? 10

SECTION – C

- 6. (a) Discuss the concept of AQL. 10
- (b) Discuss the various decision options. 10

7. Explain the following : 20

- (i) SQC
- (ii) Johnson algorithm for n-jobs.

SECTION - D

8. (a) What is the role of MIS in decision making ? 10

(b) What is product life cycle ? 10

9. Write short notes on the following : 20

- (i) Role of 3 S's
- (ii) Value Engineering.

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