97630

B.C.A. 3rd Semester (New) (Regular)

Examination-December, 2012

Micro Processor & Assembly Language Paper-BCA-203

Time: 3 hours

Max. Marks: 80

Before answering the questions, candidates should ensure that they have been supplied the correct and complete question paper. No complaint in this regard will be entertained after the examination.

- **Note**: Attempt any **five** questions in all. All questions carry equal marks.
- (a) What is a Microprocessor ? List and briefly explain the specific features of microprocessor architecture.
 - (b) What is meant by Input/Output interfacing? State its types as well as the importance of each type of interfacing.

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- 2. (a) What is UART? What does it do? Is it mandatory to correct the clock to the input of UART's receiver? Explain.
 - (b) What is interrupt ? What are various types of interrupts ? Discuss the relevance of each category.
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- 3. Explain the following:
 - (a) Bus architecture

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(b) 8255A PPI

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- 4. (a) What do you mean by 8086 pin diagram? Explain the operation of each pin by giving suitable illustration.
 - (b) Why assembly language is given a special treatment over other languages? Also state the areas of applications where A/Language is best suited.
- 5. (a) What are addressing modes? State the necessity and adoption of addressing modes in 8086 A/L programming by taking suitable examples. 8

(b)	What is 8086 instruction set? Classify
	the instruction set and discuss at least 2
	instructions of each category along with
	their usage.
(a)	What is the structure of 8086/8088
	assembly language program ? Discuss

- 6. (a) What is the structure of 8086/8088 assembly language program? Discuss each and every element of this structure.
 - (b) What is 8254 chip used for ? Illustrate its working through its schematic diagram. 8
- **7.** How the following functions in 8257 DMAC are achieved:
 - (a) Multiplexing upper address and data bytes
 - (b) Demultiplexing the buses
 - (c) Entering in Slave & Master modes
 - (d) Accessing internal registers of 8255 as other memory locations or I/O ports.
- 8. Explain the following:
 - (a) 8251A PCI 8 (b) 8279 Chip. 8

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